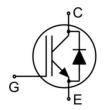


#### **Main Product Characteristics:**

Vces	700V
Ic	80A
V <sub>CE(sat)</sub>	1.6V





TO-247

Schematic Diagram

#### **Features and Benefits:**

- Trench FS technology offering
- High speed switching
- Low gate charge and V<sub>CE(sat)</sub>
- High ruggedness, temperature stable behavior
- Maximum junction temperature 175°C



## **Applications:**

- Solar Inverters
- Uninterruptible power supplies
- Motor drives
- Air condition

# **Absolute Max Rating:**

Symbol	Parameter	Value	Units	
Vces	Collector-Emitter Voltage	700	V	
V <sub>GES</sub>	Gate- Emitter Voltage	±30	V	
	Collector Current	160		
l Ic	Collector Current @T <sub>C</sub> = 100 °C	80		
I <sub>Cpuls</sub>	Pulsed Collector Current, t <sub>p</sub> limited by Tjmax	320	Α	
-	Turn off safe operating area,V <sub>CE</sub> =650V,T <sub>J</sub> =175°C	320		
1_	Diode Continuous Forward Current @Tc = 25 °C	160		
IF 	Diode Continuous Forward Current @Tc = 100 °C			
Ігм	Diode Maximum Forward Current	320		
В	Power Dissipation @ T <sub>C</sub> = 25°C	469	W	
P <sub>D</sub>	Power Dissipation @ T <sub>C</sub> = 100°C	234	W	
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +175	°C	
TL	Maximum Temperature for Soldering	260	°C	

**Version: Preliminary** 



### **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
Б	Thermal Resistance,Junction-to-case for IGBT	_	0.32	°C/W
R <sub>θJC</sub>	Thermal Resistance,Junction-to-case for Diode	_	0.44	°C/W
R <sub>θJA</sub> Thermal Resistance,Junction-to-ambient		_	40	°C/W

## Electrical Characteristics @T<sub>A</sub>=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V(BR)CES	Collector-Emitter Breakdown Voltage	700	760	_	V	Vge=0V,Ice=1mA
			1.6	1.8		Ic=80A ,VgE=15V
VCE(sat)	Collector-Emitter Saturation Voltage	_	1.0	1.0	V	@T <sub>J</sub> =25°C
V CE(sat)	Collector-Efficier Saturation Voltage	_	1.9	_		Ic=80A ,VgE=15V
						@T <sub>J</sub> =175°C
V <sub>GE(th)</sub>	Gate Threshold Voltage	4.5	5.5	6.5	V	Ic=400µA,Vc==Vg=
Ices	Collector-Emitter Leakage Current	_		1	μA	Vge =0V,Vce=650V
loro	Gate to Emitter Reverse Leakage	_	_	100	nA	VGE=30V,VCE=0V
IGES	Gate to Efficient Reverse Leakage	_	_	-100	IIA	V <sub>GE</sub> =-30V,V <sub>CE</sub> =0V
Cies	Input capacitance	_	6100	_		V <sub>GS</sub> = 0V
Coes	Output capacitance	_	245	_	pF	V <sub>DS</sub> = 25V
Cres	Reverse transfer capacitance	_	145	_		f = 1MHz
t <sub>d(on)</sub>	Turn-on delay time	_	91	_		
t <sub>r</sub>	Rise time	_	34	_		Vcc=400V,Ic=75A,
t <sub>d(off)</sub>	Turn-Off delay time	_	323	_	ns	$V_{GE}=0/15V$ , $R_g=8\Omega$ ,
t <sub>f</sub>	Fall time	_	53	_		
Eon	Turn-On Switching Loss	_	1.6	_		\/aa=400\/ la=90A
Eoff	Turn-Off Switching Loss	_	1.2	_	mJ	Vcc=400V,Ic=80A, Vg==0/15V, Rg=5Ω,
Ets	Total Switching Loss	_	2.8	_		VGE-0/13V, Kg-312,
Qg	Total Gate Charge	_	219	_		\/400\/
Qge	Gate to Emitter Charge	_	49	_	nC	Vcc=480V, Ic=80A, VGE=15V
Qgc	Gate to Collector Charge	_	90	_		VGE-10V
	Short circuit collector current Max.1000					\/ -15\/\/ < 100\/
Ic(sc)	short circuits Time between short circuits:	_	450	_	Α	V <sub>GE</sub> =15V,V <sub>CC</sub> ≤400V, t <sub>sc</sub> ≤11μs,T <sub>J</sub> ≤150°C
	≥1.0s					ι <sub>sc</sub> ≪ 11μ5,1J≪ 100 €

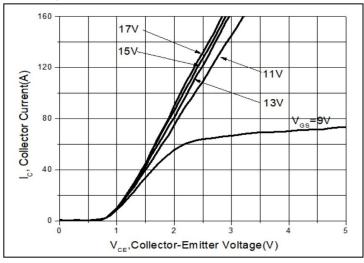
# Electrical Characteristics of the Diode@T<sub>A</sub>=25°C unless otherwise specified

Symbol	Parameter	Min.	Max.	Units	Conditions
t <sub>rr</sub>	Reverse Recovery Time	_	85	ns	
Q <sub>rr</sub>	Reverse Recovery Charge	_	1.1	μC	$T_J = 25^{\circ}C$ , $I_F = 37.5A$ , $di/dt =$
Irrm	Diode Peak Reverse Recovery	_	23	А	1000A/μs,Lσ=30nH
	Current				





# **Typical Electrical and Thermal Characteristics**



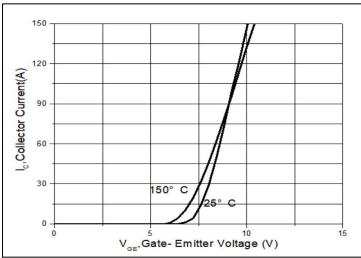
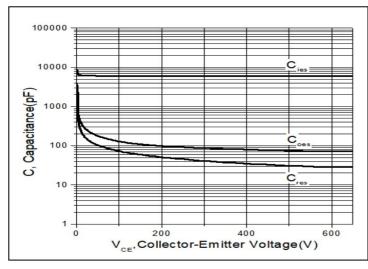


Figure 1. Typical Output Characteristics

Figure 2. Typical Transfer Characteristics



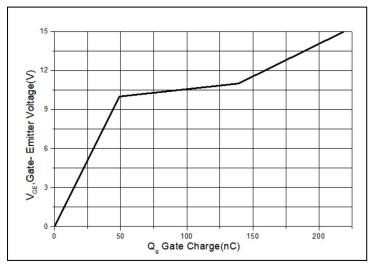


Figure3. Typical Capacitance

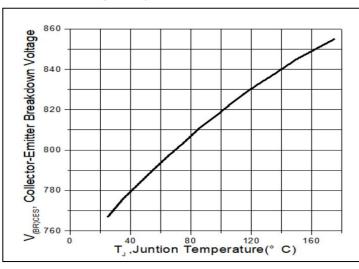


Figure 4. Typical Gate Charge

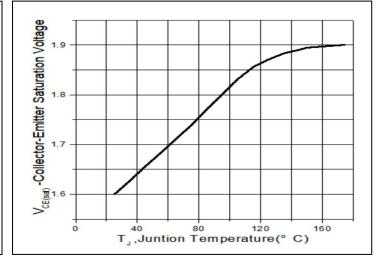
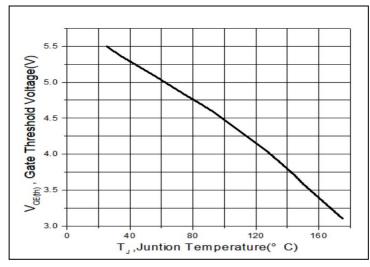


Figure 5. Collector-Emitter Breakdown Voltage vs. Temperature

Figure 6. Collector-Emitter Saturation Voltage vs. Temperature



# **Typical Electrical and Thermal Characteristics**



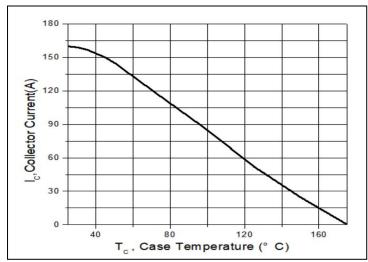
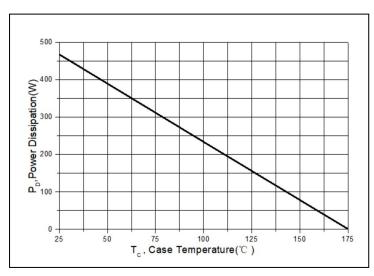


Figure 7. Gate Threshold Voltage vs. Temperature

Figure 8. Collector Current vs. Temperature



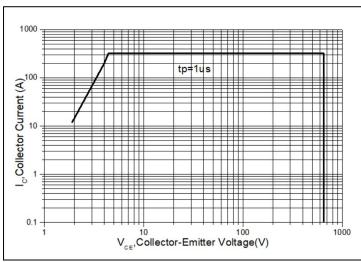
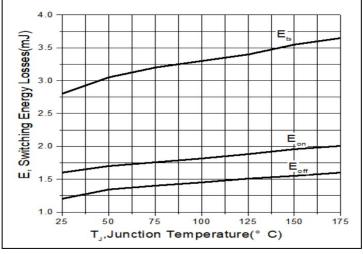


Figure 9. Power Dissipation vs. Case Temperature

Figure 10. Forward Bias Safe Operating Area



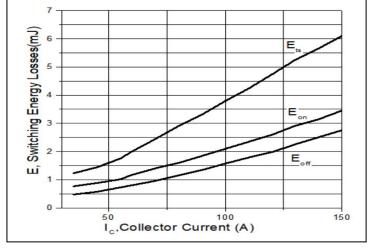


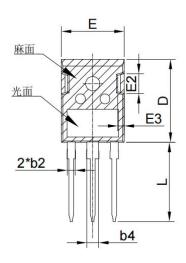
Figure 11. Switching Energy vs. Temperature

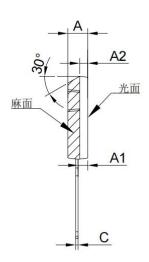
Figure 12. Switching Loss vs.  $I_{\text{C}}$ 

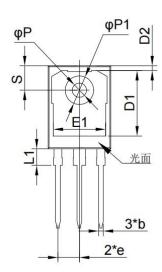


### **Mechanical Data:**

#### Unit:mm







			TO24	17-3L			
	Min	Тур	Max		Min	Тур	Max
Α	4.7	5.00	5.20	E1	13.2	13.5	13.8
A1	2.30	2.40	2.50	E2	4.90	5.00	5.10
A2	1.90	2.00	2.10	E3	1.50	1.60	1.70
b	1.10	1.20	1.30	е	5.34	5.44	5.54
b2	1.80	2.00	2.20	L <sub>2</sub>	19.80	20.00	20.32
b4	2.80	3.00	3.20	L1		4.17	4.50
С	0.5	0.6	0.7	Р	3.50	3.60	3.70
D	20.8	20.95	21.1	P1	7.00	7.19	7.40
D1	16.25	16.55	16.85	S	6.04	6.15	6.3
D2	0.95	1.17	1.35				
E	15.48	15.88	16.28				





#### **ATTENTION:**

- Any and all Silikron products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Silikron representative nearest you before using any Silikron products described or contained herein in such applications.
- Silikron assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Silikron products described or contained herein.
- Specifications of any and all Silikron products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- Silikron Microelectronics (Suzhou) Co.,Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all Silikron products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Silikron Microelectronics (Suzhou) Co.,Ltd.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Silikron believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Silikron product that you intend to use.

**Version:Preliminary**