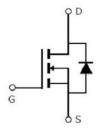
SSFT4003/SSFT4003A

Main Product Characteristics:

V _{DSS}	40V		
R _{DS} (on)	2.4mΩ(typ.)		
I _D	200A ①		







TO-220 SSFT4003

TO-263 SSFT4003A

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 175°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units	
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V①	200		
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V①	135	Α	
I _{DM}	Pulsed Drain Current②	750		
D @TC - 25°C	Power Dissipation③	220	W	
P _D @TC = 25°C	Linear Derating Factor	1.5	W/°C	
V _{DS}	Drain-Source Voltage	40	V	
V _{GS} Gate-to-Source Voltage		± 24	V	
E _{AS}	Single Pulse Avalanche Energy @ L=0.3mH	912	mJ	
I _{AS}	Avalanche Current @ L=0.3mH	78	Α	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	

SSFT4003/SSFT4003A

Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _θ JC	Junction-to-case③	_	0.62	°C/W
D	Junction-to-ambient (t ≤ 10s) ④	_	60	°C/W
R ₀ JA	Junction-to-Ambient (PCB mounted, steady-state) ④	_	40	°C/W

Electrical Characterizes $@T_A=25$ $^{\circ}$ C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
D	Static Drain-to-Source on-resistance		2.4	3.5	m0	V _{GS} =10V,I _D = 30A
R _{DS(on)}	Static Drain-to-Source on-resistance		4.1	_	mΩ	T _J = 125℃
V	Cata threshold valtage	2	_	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
$V_{GS(th)}$	Gate threshold voltage	_	2.0	_	V	T _J = 125℃
1	Drain to Source leakage current	_	_	1		V _{DS} = 40V,V _{GS} = 0V
I _{DSS}	Drain-to-Source leakage current		_	50	μA	T _J = 125°C
1	Cata to Causaa famuland laaka sa		_	100	A	V _{GS} =20V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -20V
Qg	Total gate charge	_	104	_		I _D = 75A,
Q _{gs}	Gate-to-Source charge	_	16	_	nC	V _{DS} 32V,
Q_{gd}	Gate-to-Drain("Miller") charge		40	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time		21.4	_		V _{GS} =10V, V _{DS} =20V,
tr	Rise time	_	57.8	_		R _L =0.26Ω,
t _{d(off)}	Turn-Off delay time	_	48.7	_	ns	$R_{GEN}=3.0\Omega$,
t _f	Fall time	_	19.9	_		I _D = 75A
C _{iss}	Input capacitance	_	7615	_		V _{GS} = 0V,
Coss	Output capacitance	_	959	_	pF	V _{DS} = 25V,
C _{rss}	Reverse transfer capacitance		342			f = 1MHz

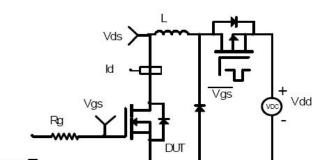
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode) ①	_	_	200	Α	MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode)	_	_	750	А	integral reverse p-n junction diode.
V _{SD}	Diode Forward Voltage	_	0.86	1.3	V	I _S =30A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	29.6	_	ns	$T_J = 25^{\circ}C$, $I_F = 50A$, $di/dt =$
Q _{rr}	Reverse Recovery Charge	_	22.2	_	nC	100A/µs

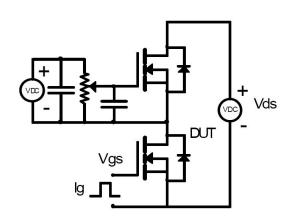


Test Circuits and Waveforms

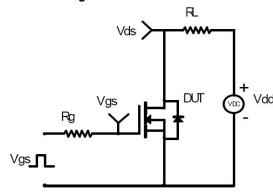
EAS Test Circuit:



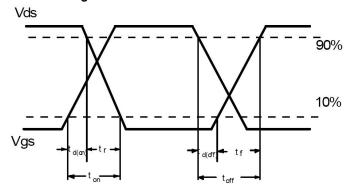
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Wavefor

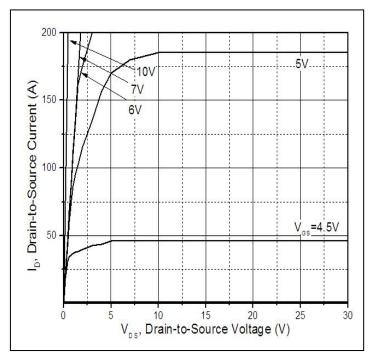


Notes:

- ①Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.
- ②Repetitive rating; pulse width limited by max junction temperature.
- ③The power dissipation PD is based on max junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\texttt{9JA}}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C



Typical Electrical and Thermal Characteristics



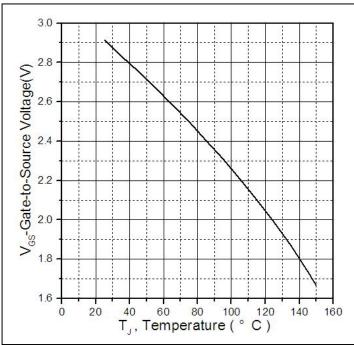
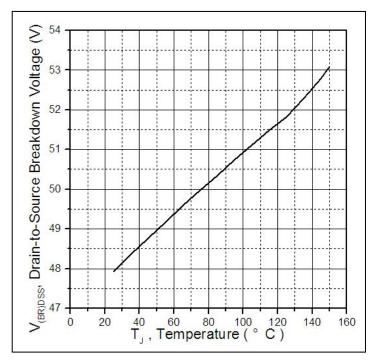
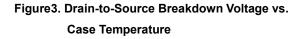


Figure 1. Typical Output Characteristics

Figure 2. Gate to source cut-off Voltage





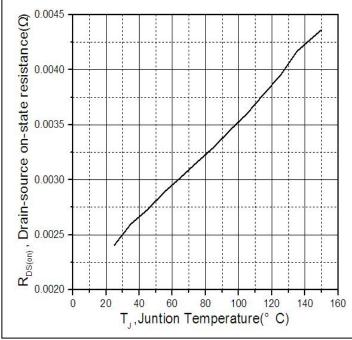
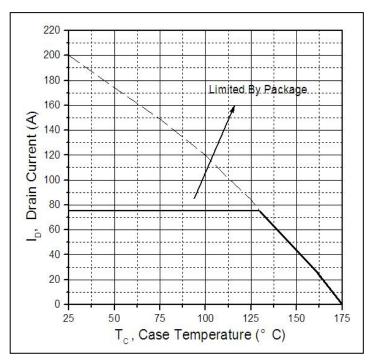


Figure4.Normalized On-Resistance vs. Case Temperature



Typical Electrical and Thermal Characteristics



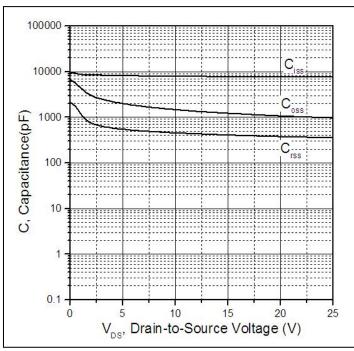


Figure 5. Maximum Drain Current vs. Case Temperature

Figure 6. Typical Capacitance vs. Drain-to-Source Voltage

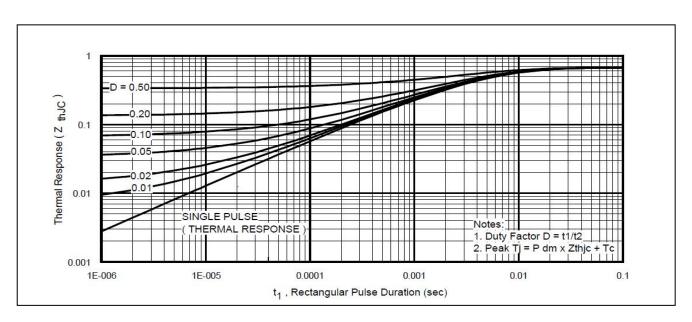
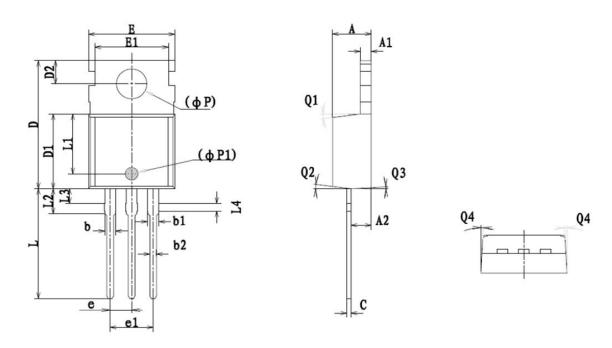


Figure 7. Maximum Effective Transient Thermal Impedance, Junction-to-Case



Mechanical Data:

TO-220 Package Outline Demension(Unit:mm)

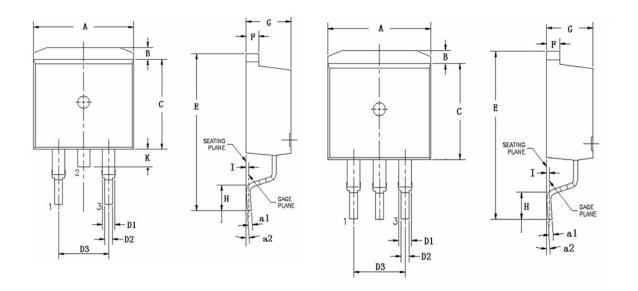


Symbol	Dimens	ion In Mil	limeters	Dimension In Inches		
Symbol	Min	Nom	Max	Min	Nom	Max
Α	4.400	4.550	4.700	0.173	0.179	0.185
A1	1.270	1.300	1.330	0.050	0.051	0.052
A2	2.240	2.340	2.440	0.088	0.092	0.096
b	2 — 2	1.270		86 - 8	0.050	
b1	1.270	1.370	1.470	0.050	0.054	0.058
b2	0.750	0.800	0.850	0.030	0.031	0.033
C	0.480	0.500	0.520	0.019	0.020	0.021
D	15.100	15.400	15.700	0.594	0.606	0.618
D1	8.800	8.900	9.000	0.346	0.350	0.354
D2	2.730	2.800	2.870	0.107	0.110	0.113
E	9.900	10.000	10.100	0.390	0.394	0.398
E1	5=1	8.700		85-8	0.343	
ФР	3.570	3.600	3.630	0.141	0.142	0.143
ФР1	1.400	1.500	1.600	0.055	0.059	0.063
е	2.54BSC				0.1BSC	*
e1	5.08BSC				0.2BSC	
L	13.150	13.360	13.570	0.518	0.526	0.534
L1	7.35REF				0.29REF	
L2	2.900	3.000	3.100	0.114	0.118	0.122
L3	1.650	1.750	1.850	0.065	0.069	0.073
L4	0.900	1.000	1.100	0.035	0.039	0.043
Q1	5°	7º	90	5°	7º	9°
Q2	5°	7º	9°	5°	7º	90
Q3	5°	7º	90	5°	7º	90
Q4	10	30	5°	1º	30	5°





TO-263 Package Outline Demension(Unit:mm)



Symbol	Dimension I	n Millimeters	Dimension In Inches		
	Min	Max	Min	Max	
Α	9.660	10.280	0.380	0.405	
В	1.020	1.320	0.040	0.052	
С	8.590	9.400	0.338	0.370	
D1	1.140	1.400	0.045	0.055	
D2	0.700	0.950	0.028	0.037	
D3	5.080 (TYP)		0.200	(TYP)	
E	15.090	15.390	0.594	0.606	
F	1.150	1.400	0.045	0.055	
G	4.300	4.700	0.169	0.185	
Н	2.290	2.790	0.090	0.110	
1	0.250	0.250 (TYP)		(TYP)	
K	1.300	1.600	0.051	0.063	
a1	0.450	0.650	0.018	0.026	
a2	00	8 ⁰	1 ⁰	8 ⁰	



SSFT4003/SSFT4003A

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