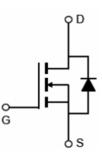


### Main Product Characteristics:

| V <sub>DSS</sub>     | 100V       |
|----------------------|------------|
| R <sub>DS</sub> (on) | 90mΩ(typ.) |
| I <sub>D</sub>       | 10A        |







TO-252 (DPAK)

Marking and Pin Assignments

Schematic Diagram

### **Features and Benefits:**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



## **Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

## **Absolute Max Rating:**

| Symbol                                 | Parameter  | Max.        | Units |
|--|--|-------------|-------|
| I <sub>D</sub> @ T <sub>C</sub> = 25°C | Continuous Drain Current, V <sub>GS</sub> @ 10V① | 10          | A     |
| I <sub>DM</sub>                        | Pulsed Drain Current②                            | 40          | A     |
| $P_{D} @T_{C} = 25^{\circ}C$           | Power Dissipation3                               | 24          | W     |
| V <sub>DS</sub>                        | Drain-Source Voltage                             | 100         | V     |
| V <sub>GS</sub>                        | Gate-to-Source Voltage                           | ± 20        | V     |
| E <sub>AS</sub>                        | Single Pulse Avalanche Energy @ L=22mH           | 77          | mJ    |
| T <sub>J</sub> T <sub>STG</sub>        | Operating Junction and Storage Temperature Range | -55 to +150 | °C    |



# **Thermal Resistance**

| Symbol           | Characterizes     | Тур. | Max. | Units |
|------------------|-------------------|------|------|-------|
| R <sub>θJC</sub> | Junction-to-case3 | _    | 6.3  | °C/W  |

### **Electrical Characterizes**@ $T_A=25$ °C unless otherwise specified

| Symbol               | Parameter                            | Min. | Тур. | Max. | Units | Conditions  |
|----------------------|--------------------------------------|------|------|------|-------|---|
| V <sub>(BR)DSS</sub> | Drain-to-Source breakdown voltage    | 100  | _    | —    | V     | $V_{GS} = 0V, I_D = 250 \mu A$                        |
| D                    | Statia Drain ta Course en registance | _    | 90   | 110  | mΩ    | V <sub>GS</sub> =10V,I <sub>D</sub> =5A               |
| $R_{DS(on)}$         | Static Drain-to-Source on-resistance | _    | 95   | 140  |       | V <sub>GS</sub> =4.5V,I <sub>D</sub> =3A              |
| $V_{GS(th)}$         | Gate threshold voltage               | 1    | —    | 2.5  | V     | $V_{DS}=V_{GS}$ , $I_D = 250 \mu A$                   |
| I <sub>DSS</sub>     | Drain-to-Source leakage current      | _    | —    | 1    | μA    | $V_{DS}$ =100V, $V_{GS}$ =0V                          |
|                      |                                      | _    | —    | 100  |       | V <sub>GS</sub> =20V                                  |
| I <sub>GSS</sub>     | Gate-to-Source forward leakage       | _    | —    | -100 | nA    | V <sub>GS</sub> = -20V                                |
| Qg                   | Total gate charge                    | _    | 10   | —    |       | $I_D = 5A,$<br>$V_{DS}=30V,$                          |
| $Q_{gs}$             | Gate-to-Source charge                | _    | 2    | —    | nC    |   |
| $Q_{gd}$             | Gate-to-Drain("Miller") charge       | _    | 2.2  | —    |       | $V_{GS} = 10V$  |
| t <sub>d(on)</sub>   | Turn-on delay time                   | _    | 7.4  | —    |       | $V_{GS}$ =10V, $V_{DS}$ =30V,<br>R <sub>GEN</sub> =3Ω |
| t <sub>r</sub>       | Rise time                            |      | 10   | —    |       |   |
| t <sub>d(off)</sub>  | Turn-Off delay time                  | _    | 22   | _    | ns    |   |
| t <sub>f</sub>       | Fall time                            | _    | 2.8  | —    |       | I <sub>D</sub> = 10A                                  |
| C <sub>iss</sub>     | Input capacitance                    | _    | 825  | —    |       | $V_{GS} = 0V$   |
| Coss                 | Output capacitance                   | _    | 30   | —    | pF    | V <sub>DS</sub> = 50V                                 |
| C <sub>rss</sub>     | Reverse transfercapacitance          | _    | 23   |      |       | f = 1MHz  |

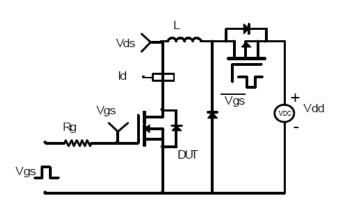
# **Source-Drain Ratings and Characteristics**

| Symbol          | Parameter                 | Min. | Тур. | Max. | Units | Conditions                              |  |
|-----------------|---------------------------|------|------|------|-------|---|--|
| Is              | Continuous Source Current | —    | Ι    | 10   | A     | MOSFET symbol                           |  |
|                 | (Body Diode)              |      |      |      |       | showing the                             |  |
| I <sub>SM</sub> | Pulsed Source Current     | _    | _    | 40   | A     | integral reverse                        |  |
|                 | (Body Diode)              |      |      |      |       | p-n junction diode.                     |  |
| V <sub>SD</sub> | Diode Forward Voltage     | —    | —    | 1.2  | V     | I <sub>S</sub> =3A, V <sub>GS</sub> =0V |  |
| t <sub>rr</sub> | Reverse Recovery Time     | —    | 20   | _    | ns    | I <sub>S</sub> =10A,di/dt=100A/us       |  |
| Q <sub>rr</sub> | Reverse Recovery Charge   | _    | 20   |      | nC    | $I_{S} = 10A, dI/dl = 100A/dS$          |  |

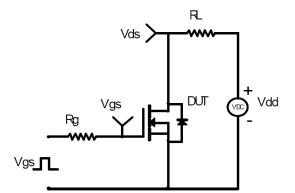


## **Test Circuits and Waveforms**

EAS Test Circuit:

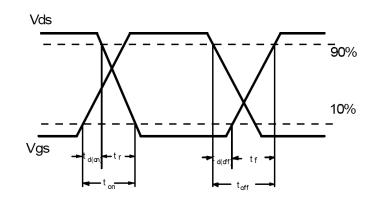


Switching Time Test Circuit:



Switching Waveforms:

Gate Charge Test Circuit:

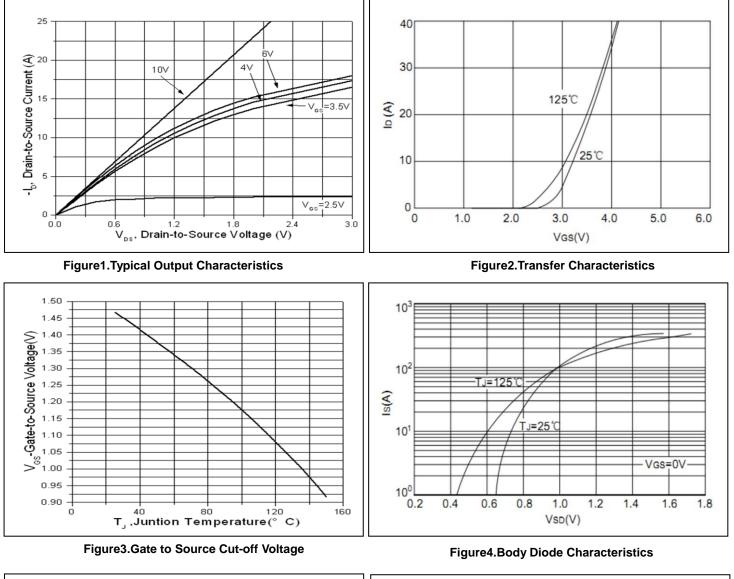


#### Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- 2 Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



# **Typical Electrical and Thermal Characteristics**



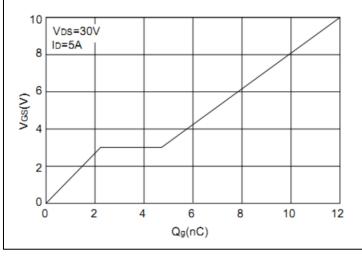
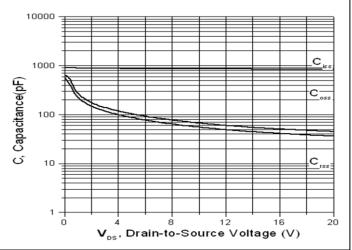


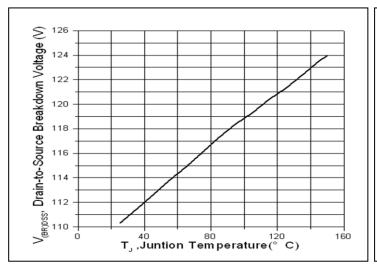
Figure5.Gate Charge



#### Figure6.Capacitance



## **Typical Electrical and Thermal Characteristics**



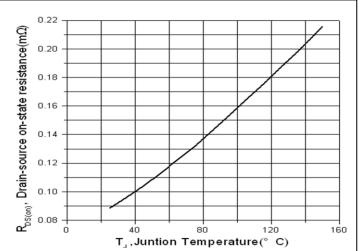


Figure7.Drain-to-Source Breakdown Voltage vs. Junction

Figure8.Normalized On-Resistance vs. Junction Temperature

#### Temperature

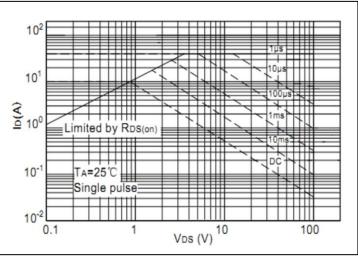


Figure9.Safe Operating Area

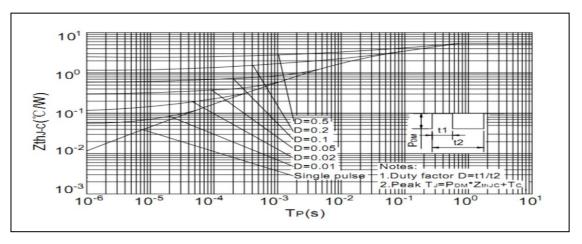
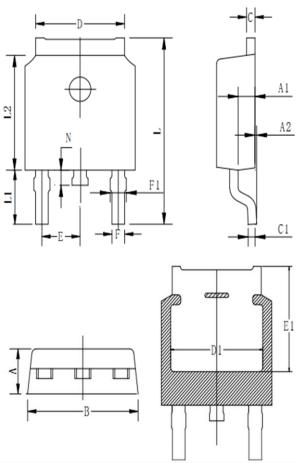


Figure10.Normalized Maximum Transient Thermal Impedance



## **Mechanical Data:**

TO-252 Package Outline(Unit:mm)



| Symbol     | Min  | Тур  | Max   |
|------------|------|------|-------|
| A          | 2.20 | 2.30 | 2.40  |
| A1         | 0.91 | 1.01 | 1.11  |
| A2         |      |      | 0.25  |
| В          | 6.50 | 6.60 | 6.70  |
| C          | 0.40 | 0.50 | 0.60  |
| C1         | 0.40 | 0.50 | 0.60  |
| D          | 5.15 | 5.30 | 5.45  |
| D1         | 5.10 | 5.25 | 5.40  |
| E          | 2.20 | 2.29 | 2.40  |
| E1         | 4.95 | 5.15 | 5.35  |
| F          | 0.66 | 0.76 | 0.86  |
| <b>F</b> 1 | 0.70 | 0.82 | 0.95  |
| L          | 9.70 | 9.90 | 10.10 |
| L1         | 2.67 | 2.87 | 3.07  |
| L2         | 6.00 | 6.10 | 6.20  |
| N          | 0.60 | 0.80 | 1.00  |





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